IN THE SPECIFICATION:

Please replace paragraph [02] with the following amended paragraph:

[02]   This application is related to the following U.S. patent applications, each of which is hereby incorporated by reference as if set forth in full in this document for all purposes:

Serial no. 09/815,122, entitled "Adaptive Integrated Circuitry with Heterogeneous and Reconfigurable Matrices of Diverse and Adaptive Computational Units having Fixed, Application Specific Computational Elements," filed on March 22, 2001[[;]]. now U.S. patent 6,836,839 issued December 28, 2004; the present application is a continuation-in-part of the 09/815,122 application; and

Serial no. 10/443,554, entitled "Uniform Interface for a Functional Node in an Adaptive Computing Engine," filed on May 21, 2003.

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